DAYANANDA SAGAR COLLEGE OF ARTS SCIENCE AND COMMERCE

Internal Quality Assurance Cell (IQAC) Cell

Guest Lecture on

"Thread Level Parallelism in Modern CPUs – Concepts, Tradeoffs and Real World Implementations"

Department: Department of Computer Applications - MCA **Date: 24/2/2026**

Sl. No.	Particulars	Event related Details			
1.	Event	Guest Lecture			
2.	Title of the Event	Thread Level Parallelism in Modern CPUs – Concepts, Tradeoffs and Real World Implementations			
3.	Date	24th February 2026			
4.	Time	11:00 AM to 1:00 PM			
5.	Venue	Seminar Hall, Building number 17			
6.	Resource Person 1 Details	Dr. Shruthi BV, Associate Professor, CSE(AI & ML), Global Academy of Technology, Bengaluru.			
7.	Topics Covered	Introduction to Parallel Computing, Concepts of Threads, Thread level parallelism, Types of Multithreading, Hardware & Software Support, Performance Considerations, Applications, Challenges, Future Trends			
8.	Resource Person 2 Details	NA			
9.	Topics Covered				
10.	No. Faculty Participants	Internal:	2	External:	
11.	No. Student Participants	Internal:	79	External:	
12.	Faculty Coordinator/s	1. Dr. Nithya BN, Associate Professor, Department of Computer Applications, DSCASC. 2. Prof. Akshatha, Assistant Professor, Department of Computer Applications, DSCASC.			
13.	Student Coordinator/s	NA			
14.	Total Expenditure	NA			
15.	Sponsors and Amount (if any)	NA			
16.	Program Schedule of	NA			

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Sl. No.	Particulars	Event related Details
	the Event attached?	
17.	Report uploaded on college website?	Yes
18.	Report sent to media? If yes, give details:	NA
19.	Report uploaded in Social Media?	NA
20.	Certificates Printed?	NA
21.	Feedback Collected?	Yes
22.	Attendance Sheet Attached?	Yes
23.	Summary of the Event	The Guest Lecture on "Thread Level Parallelism in Modern CPUs" was organized by the Department of Computer Applications (MCA) at Dayananda Sagar College of Arts, Science and Commerce (DSCASCO), Bengaluru, under IQAC. It took place on 24th February 2026 from 11:00 AM to 1:00 PM in Building No. 17 Seminar Hall. Dr. Shrututi B V, Associate Professor in CSE (AI & ML) at Global Academy of Technology, Bengaluru, served as the resource person. She delivered an engaging session on Computer Organisation and Architecture, emphasizing Thread Level Parallelism (TLP) in contemporary CPUs.The lecture covered essential topics on the concept These were discussed with focus on concepts, trade-offs, and practical implementations, offering students valuable insights into modern processor design. A total of 79 students from I Semester MCA (A and B sections) participated enthusiastically, benefiting from the expert guidance and interactive Q&A. The event was effectively coordinated and significantly enhanced the students' knowledge in advanced computing architecture.
24.	Photographs of the Event	Enclosed Below

Quality Assurance Cell Bengaluru 560 111 DSCASC

Event Coordinator

Deputy Director

24/02/26

IQAC Coordinator

IQAC Co-ordinator
Dayananda Sagar College of Arts, Science & Commerce
Kumara[illegible] Layout, Bengaluru - 560 111

Principal

Principal
Dayananda Sagar College of Arts, Science & Commerce
Kumaraswamy Layout, Bengaluru - 560 111

Brochure

DAYANANDA SAGAR
COLLEGE OF ARTS, SCIENCE AND COMMERCE
SHAVIGE MALLESHWARA HILLS, KUMARASWAMY LAYOUT
IQAC

Department of Computer Applications-MCA

Guest lecture on

Computer Organisation and Architecture

THREAD LEVEL PARALLELISM IN MODERN CPU'S CONCEPTS,TRADE-OFF AND REAL WORLD IMPLEMENTATIONS.

Dr. Shruti B V

Associate Professor CSE(AI&ML)
Global Academy of Technology
Bangalore

DATE & TIME
24TH FEB 2026
11:00AM TO 1:00PM

VENUE
SEMINAR HALL, BUILDING NO 17

EVENT COORDINATOR | DEPUTY DIRECTOR | VICE PRINCIPAL | PRINCIPAL

Attendance

DAYANANDA SAGAR COLLEGE OF ARTS, SCIENCE AND COMMERCE

Shavige Malleshwara Hills, Kumaraswamy Layout, Bangalore-560082.

Department of Computer Applications – MCA

Thread Level Parallelism in Modern CPUs Concepts, Trade – offs and Real World Implementations

Date: 24/02/2026

Time:11:00 AM to 1:00 PM

Attendance Sheet

S. No.	USN	Name of the Student	Signature
1.	P03CJ25S126055	Gaurijan Rana	
2.	P03CJ25S126095	THRIPTHI. J	
3.	P03CJ25S126118	Uday. K	
4.	P03CJ25S126107	Vinith. R	
5.	P03CJ25S126106	Vishal. P. Shinde	
6.	P03CJ25S126109	Vivek S Khairoor	
7.	P03CJ25S126093	Tarun. V. Gopal	
8.	P03CJ25S126094	TEJAS. N	
9.	P03CJ25S126096	Tilak. S	
10.	P03CJ25S126059	Pavan Kumar. M. M	
11.	P03CJ25S126060	Puneeth Kumar T.	
12.	P03CJ25S126103	Vinay. B. S	
13.	P03CJ25S126032	Het Kundariya	
14.	P03CJ25S126033	Jeet Kundariya	
15.	P03CJ25S126041	Mitesh Detroja	
16.	P03CJ25S126108	Vishwam Doshi	
17.	P03CJ25S126061	Rahul Bhoraniya	
18.	P03CJ25S126026	Jairaj. A	
19.	P03CJ25S126100	V. Rishi	

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20.	[illegible]	Rahul .R	
21.	[illegible]	Yashas .V	
22.	PO3CJ25512604	Rahul.K.K	
23.	PO3CJ25512605	Shubham Dwivedi	
24.	PO3CJ25512606	Shashank R	
25.	PO3CJ25512607	Shreyas HK	
26.	PO3CJ25512610	Vinay DJ	
27.	PO3CJ24SD0009	B Tanhif unnat	
28.	PO3CJ215126112	chetan kumar H	
29.	PO3CJ25512 6056	Musaib Ahmed	
30.	PO3CJ25512 6014	chaithrashree.M	
31.	PO3CJ25512 6046	M. Bhuvan	
32.	PO3CJ25512 6002	Aragha.P. Shanbhag	
33.	PO3CJ25512 6054	Nabha Mateen	
34.	PO3CJ25512 6031	Kishore	
35.	PO3CJ25512 6007	Arjun k.J	
36.	PO3CJ25512 6021	Hanthith. S.A	
37.	PO3CJ25512 6040	Maheshwari N	
38.	PO3CJ25512 6006	A Sri Varshini Reddy	
39.	PO3CJ25512 6001	Abhignya.S Iyengar	
40.	PO3CJ25512 6011	Bhanupriya. K S	
41.	PO3CJ25512 6028	Kirti Hosamath.	
42.	PO3CJ25512 6013	Rajyalaxmi Nayak.	
43.	PO3CJ25512 6065	Rajeshwari Hegde	
44.	PO3CJ25512 6061	Priyanka Leonia	

45.	[illegible]	[illegible]	[illegible]
46.	[illegible]	[illegible]	[illegible]
47.	[illegible]	[illegible]	[illegible]
48.	[illegible]	[illegible]	[illegible]
49.	[illegible]	[illegible]	[illegible]
50.	PO3CJ25S126012	Sahana H.P	[illegible]
51.	[illegible]	[illegible]	[illegible]
52.	PO3CJ25S126018	Tulasi.HR	[illegible]
53.	PO3CJ25S126007	Rakshitha CM	[illegible]
54.	PO3CJ25S126062	RAGHAVI.S.P	Raghavi.S.P
55.	PO3CJ25S126102	Vaishnavi	[illegible]
56.	PO3CJ25S126083	Suchithra.H.C	Suchithra
57.	PO3CJ25S126078	Sowya.G.C	Sowya
58.	PO3CJ25S126097	Triveni.I.A	Triveni.I.A
59.	PO3CJ25S126113	Rajeshwari.N	[illegible]
60.	PO3CJ25S126077	Sanidhya.H.Y	[illegible]
61.	PO3CJ25S126086	Sruthi	[illegible]
62.	PO3CJ25S126050	Nishanika.M	[illegible]
63.	PO3CJ25S126003	Akanksha.V.U	[illegible]
64.	PO3CJ25S126049	Neha.A.Kalaburagi	[illegible]
65.	PO3CJ25S126020	G. Nayana	[illegible]
66.	PO3CJ25S126057	P. Raziya Mahthaz Begum	P Raziya
67.	PO3CJ25S126095	Mandara.M.	Mandara.M
68.	PO3CJ25S126053	N Raghuh	[illegible]
69.	PO3CJ25S126058	Pruthvish	[illegible]

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70.	P03CJ25S126016	Chinmaya MD	
71.	P03CJ25S126019	G. Shivakar Gowda	
72.	P03CJ25S126043	Pooja Tha M.S	
73.	P03CJ25S126042	Moha. D C	
74.	P03CJ25S126051	Nischitha. L.N.	
75.	P03CJ25S126035	Likitha. S	
76.	P03CJ25S126037	Hemavathi. M.J	
77.	P03CJ25S126047	N. Archana	
78.	P03CJ25S126038	MAHANTGOUDA V PATIL	
79.	P03CJ25S126092	SUMANTH HR	
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(Dr. NithyaBN)

(Akshatha)

Internal Quality Assurance Cell
Bengaluru
560 111
CACSC

Photographs of the Event


GPS Map Camera
Bengaluru, Karnataka, India
Wh58+29x, Dayanand Sagar College Rd, 1st Stage, Kumaraswamy Layout, Bengaluru, Karnataka 560111, India
Lat 12.907348° Long 77.565966°
Tuesday, 24/02/2026 11:38 AM GMT +05:30
Google


GPS Map Camera
Bengaluru, Karnataka, India
Shavige Malleshwara Hills, 1st Stage, Kumaraswamy Layout, Bengaluru, Karnataka 560111, India
Lat 12.907194° Long 77.56597°
Tuesday, 24/02/2026 12:15 PM GMT +05:30
Google


Bengaluru, Karnataka, India
Shavige Malleshwara Hills, 91st Main Rd, 1st Stage, Kumaraswamy Layout, Bengaluru, Karnataka 560111, India
Lat 12.907634° Long 77.565817°
Tuesday, 24/02/2026 12:17 PM GMT +05:30
Google


USE CASE: ROBOTICS & REAL-TIME SYSTEMS
Bengaluru, Karnataka, India
Shavige Malleshwara Hills, 91st Main Rd, 1st Stage, Kumaraswamy Layout, Bengaluru, Karnataka 560111, India
Lat 12.907637° Long 77.565827°
Tuesday, 24/02/2026 12:17 PM GMT +05:30
Google


GPS Map Camera
Bengaluru, Karnataka, India
Shavige Malleshwara Hills, 91st Main Rd, 1st Stage, Kumaraswamy Layout, Bengaluru, Karnataka 560111, India
Lat 12.907624° Long 77.565769°
Tuesday, 24/02/2026 12:42 PM GMT +05:30
Google

Sayanda Sagar College of Arts, Science And Commerce
Internal quality Assurance cell
Guest lecture

"Thread level parallelism in modern CPU's - Concepts, Trade-offs and Real world implementations"

The Seminar on thread-level parallelism in modern CPU was informative and well organized. Concepts were explained clearly, making it easy to understand how multiple threads are executed in modern processors

The discussion on trade-offs such as resource sharing and performance limitations was insightful and gave a better understanding of the challenges in implementing thread level parallelism

The examples used helped relate theoretical concepts to real-world applications

Thank you, mam, for insightful Seminar

- Tulasi. HR

DAYANANDA SAGAR COLLEGE of ARTS SCIENCE AND COMMERCE

Internal Quality Assurance Cell

Guest Lecture

"Thread level Parallelism in modern CPUs- Concepts, Tradeoffs and Real world Implementations"

Feedback

Date: 24/2/2026

The Seminar on thread-level Parallelism in modern CPUs was very informative and well-organized. The concepts were Explained in a clear and simple way. which made it easier to understand how multiple threads are executed in modern Processors.

The discussion on trade-offs such as resource sharing and Performance limitations was also insightful. It gave a better understanding of the challeages involved in implementing thread-level Parallelism.

The Examples used during the lecture helped in relating theoretical concepts to real-world applications.

Thankyou mam, for the insightful seminar.

-Nabha Makeen.

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Feedback

Thread level Parallelism in Modern CPU's Concept, Trade-off & Real woold implementations

The session on this new concept was very engaging and well-structured with great PPT presentation. The speaker gave the clear introduction to the concept. The real world example helped us to understand how it actually works. The video presentation was very engaging. Moreover, discussing relevant research paper added depth to the session. We are looking forward for this kind of sessions in our futur days

Anagha. P. S
PO3CJ25512600 2

The Seminar on parallelism was informative and well structed. The presenter demonstrated a strong understanding of the topic and explained complex concepts in a way that was mostly clear. The demonstration of parallel algorithms made the concepts concrete. Comparing sequential VS parallel execution clearly showed the advantages and trade-offs.

A Srivarshini
PO3CJ25512600 6